

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. .... 09/175,051, *11/11/00*  
Priority Filing Date ..... October 19, 1998 *Susler*  
Inventor ..... John T. Moore et al.  
Assignee ..... Micron Technology, Inc.  
Priority Group Art Unit ..... 2822  
Priority Examiner ..... M. Guerrero  
Attorney's Docket No. ..... MI22-1443  
Title: Capacitor Constructions, DRAM Constructions, and Semiconductive  
Material Assemblies (As Amended)

## **PRELIMINARY AMENDMENT**

To: Assistant Commissioner for Patents  
Washington, D.C. 20231

From: David G. Latwesen (Tel. 509-624-4276; Fax 509-838-3424)  
Wells, St. John, Roberts, Gregory & Matkin P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828

## **AMENDMENTS**

### In the Title

Replace the title with --Capacitor Constructions, DRAM Constructions, and Semiconductive Material Assemblies--.

### In the Specification

At p. 1, before the "Technical Field" section, insert

RELATED PATENT DATA

This patent resulted from a divisional application of U.S. Patent Application Serial No. 09/175,051, which was filed on October 19, 1998. *F*